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Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known

Application No.	09/888,212
Filing Date	June 22, 2001
First Named Inventor	Kwok Hung Si
Art Unit	2182
Examiner Name	Not Yet Known
Attorney Docket Number	20880-05629

Sheet 1 of 1

### U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Document No. Number - Kind Code <sup>2</sup> (if known)	Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
<i>[initials]</i>	A	US-6,567,839 B1	05-20-2003	Borkenhagen, et al.
<i>[initials]</i>	B	US-6,378,018 B1	04-23-2002	Tsem, et al.
<i>[initials]</i>	C	US-6,317,774 B1	11-13-2001	Jones, et al.
<i>[initials]</i>	D	US-6,026,503	02-15-2000	Gutgold, et al.
<i>[initials]</i>	E	US-5,392,435	02-21-1995	Masui, et al.
<i>[initials]</i>	F	US-5,247,636	09-21-1993	Minnick, et al.
<i>[initials]</i>	G	US-5,163,146	11-10-1992	Antanaitis, Jr., et al.
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		US-		

### FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> - Number <sup>4</sup> Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T <sup>6</sup>

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<i>[initials]</i>	H	SCHWAN, KARSTEN and ZHOU, HONGYI; Dynamic Scheduling Of Hard Real-Time Tasks And Real-Time Threads; IEEE Transaction On Software Engineering; Vol. 18, No. 8; August 1992; pp. 736-748.	

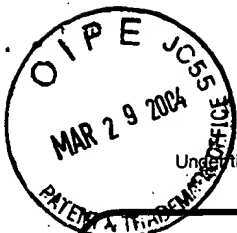
Examiner Signature	<i>[Signature]</i>	Date Considered	10/08/04
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20880/05629/DOCS/1391876.1



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<i>[initials]</i>	I	US-6,385,713 B2	05-07-2002	Yung
<i>[initials]</i>	J	US-6,366,998 B1	04-02-2002	Mohamed
<i>[initials]</i>	K	US-6,009,505	12-28-1999	Thayer, et al.
<i>[initials]</i>	L	US-5,933,650	08-03-1999	van Hook, et al.
<i>[initials]</i>	M	US-5,761,470	06-02-1998	Yoshida
<i>[initials]</i>	N	US-5,430,884	07-04-1995	Beard, et al.

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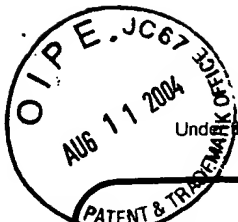
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A		US-6,728,722	04-27-2004	Shaylor
A		US-6,542,991	04-01-2003	Joy et al.
A		US-4,777,587	10-11-1988	Case et al.
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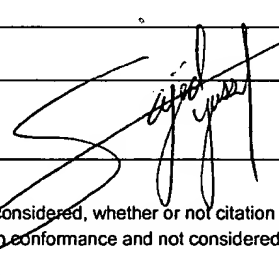
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4		Intel Corporation, Pentium Processor Family Developer's Manual, Volume 3: Architecture and Programming Manual, 1995, pages 25-1 and 25-30.	

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